

CURRICULUM VITAE

David A. Tilston
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EDUCATION:

B.A.Sc. - University of Toronto, Department of Electrical Engineering, 1978, thesis title "An Error-Correcting Code for Binary DPSK Data"

The thesis included designing and building a microprocessor-based test set-up including programming and measuring and comparing the bit-error rates with and without error-correcting codes for various signal-to-noise ratios.

EMPLOYMENT HISTORY:

2005 – current – Phoenix Antenna Systems Inc. – Senior Designer: Antenna research, design and development. Development and improvement of in-house electromagnetic modelling and antenna design tools.

2000 – 2005 – TIL-TEK Antennas (Division of Wi-LAN Inc.) – VP Operations and Technology: Similar to previous position only acting as VP after the company was sold to Wi-LAN Inc.

1996 – 2000 – TIL-TEK Antennas – Chief Operating Officer: Overall business management, budget development & tracking, execution of strategic plans.

1988 – 1996 – TIL-TEK Antennas – Director of Engineering: Antenna design, development, project and departmental management.

1982 – 1988 – Navtel Canada Inc. – Project Manager: Management of development projects for advanced protocol analyzers including real-time software & hardware architecture and design.

1980 – 1982 – Intel Semiconductor of Canada Ltd. – Field Applications Engineer: Providing applications expertise to customers on advanced and existing components, software and systems. Teaming with Field Sales Engineers to ensure business objectives were met or exceeded.

1978 – 1980 – Ferritronics Ltd. – Design Engineer – Design and development of signalling and automatic identification circuits, making use of active filters and embedded microprocessors for two-way mobile radio systems.